

# Knobbe Martens Olson & Bear LLP

*Intellectual Property Law*

2040 Main Street  
Fourteenth Floor  
Irvine, CA 92614  
Tel 949-760-0404  
Fax 949-760-9502  
www.kmob.com

*Lee W. Henderson, Ph.D.*

April 8, 2009

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Title: DYNAMIC AND DIFFERENTIAL CMOS LOGIC WITH SIGNAL-  
INDEPENDENT POWER CONSUMPTION TO WITHSTAND  
DIFFERENTIAL POWER ANALYSIS  
Letters Patent No. 7,417,468  
Issued: August 26, 2008  
Our Reference No.: UCLARF.003NP

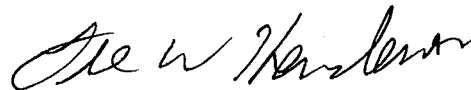
Dear Sir:

Enclosed for filing is a Certificate of Correction in connection with the above-identified patent.

As the errors cited in the Certificate of Correction were incurred through the fault of the Patent Office, no fee is believed to be required. However, please charge our Deposit Account No. 11-1410 for any fees that may be incurred with this request.

Respectfully submitted,

Knobbe, Martens, Olson & Bear, LLP



Lee W. Henderson Ph.D.  
Registration No. 41,830  
Customer No. 20995

Enclosure  
6949646  
040809

**San Diego**  
619-235-8550

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415-954-4114

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206-405-2000

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202-640-6400

# UNITED STATES PATENT AND TRADEMARK OFFICE

## CERTIFICATE OF CORRECTION

**PATENT NO.** : 7,417,468  
**APPLICATION NO.** : 10/565,551  
**ISSUE DATE** : August 26, 2008  
**INVENTOR(S)** : Verbauwhede et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 5 at approximately line 17, change "VT" to --V<sub>t</sub>--.

In column 7 at line 60, change "DPUTN" to --DPUN--.

In column 8 at line 8, after "event" insert --,--.

### MAILING ADDRESS OF SENDER:

Lee W. Henderson Ph.D.  
KNOBBE, MARTENS, OLSON & BEAR, LLP  
2040 Main Street, 14<sup>th</sup> Floor  
Irvine, California 92614

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